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Docket:

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Cameron S. Gilligan
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4-6-98
M.R.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Takeshi FUKUNAGA et al.)
Serial No. 08/781,920) Art Unit: 1112
Filed: December 30, 1996) Examiner: M. Padgett
For: METHOD OF MANUFACTURING)
A SEMICONDUCTOR DEVICE) Date: March 5, 1998

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT AND CROSS-REFERENCE OF RELATED APPLICATIONS

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application. These references were cited in the related application noted below. Please note that the article by Bonnel (IEEE Vol. 14, No. 12) is not enclosed. The undersigned will submit a copy as soon as it is available.

Pursuant to Applicant's duty of candor under 37 C.F.R. §1.56, Applicant wishes to inform the Examiner of the following applications claiming related


03/11/1998 WILLIAM 00000037 08781920
01 FC:126 technical subject matter:
240.00 GP

<u>Serial No.</u>	<u>Filing Date</u>	<u>Title</u>
08/456,667	June 1, 1995	Process for Fabricating Semiconductor Device

Submitted herewith is the requisite fee of \$240.00. Hence, it is requested that the references submitted herewith be considered and made of record in the subject application.

Copies of the references are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Respectfully submitted,



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